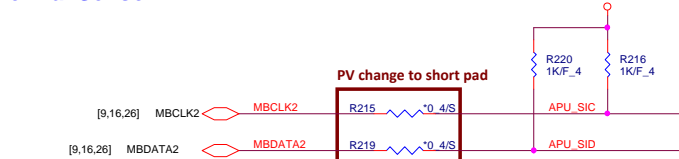
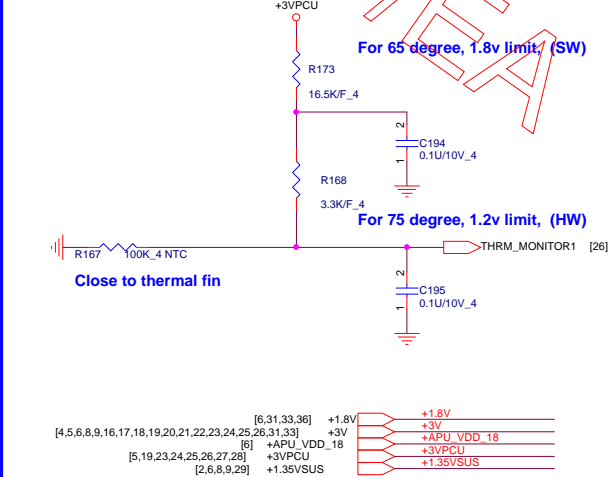


Thermal Sensor

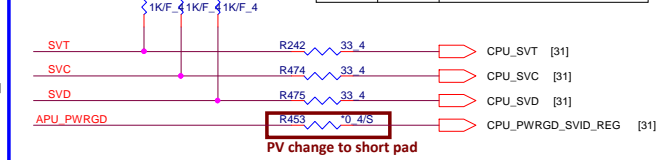


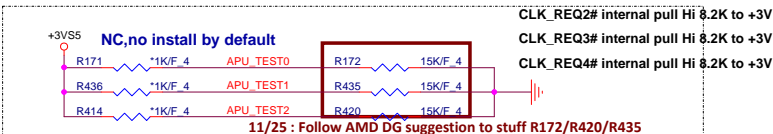
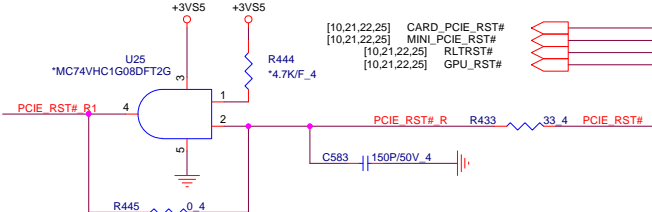
IO Thrm Protect



Serial VID

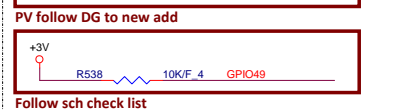
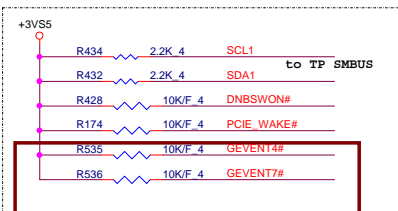
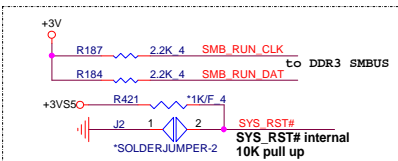
VFIX MODE		VID Override table (VDD)
SVC	SVD	Boot Voltage
0	0	1.1V
0	1	1.0V
1	0	0.9V
1	1	0.8V



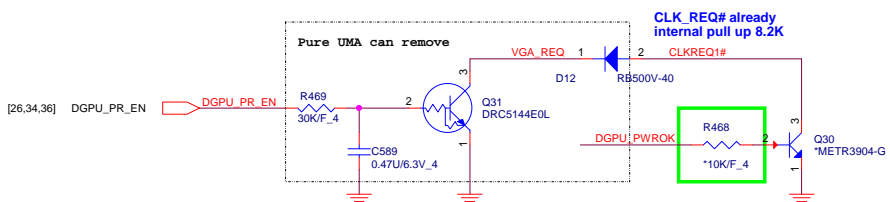


11/25 : Follow AMD DG suggestion to stuff R172/R420/R435

TEST2	TEST1	TEST0	Description
0	0	0	FCH TAP accessible from APU when TAPEN is asserted FCH JTAG pins are overloaded for multiple functions, in this configuration the FCH JTAG are used as non-JTAG pins
0	0	1	Reserved
0	1	X	Reserved
1	TMS	0	FCH JTAG multi-function pins are configured as JTAG pins, in this configuration the FCH TAP can be accessed from FCH JTAG pins
1	TMS	1	Use on ATE only Yuba JTAG enabled



Follow sch check list



BOARD ID SETTING

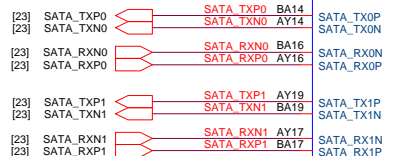
Board ID 0	Definition
0	UMA
1	SG

Board ID [2:1]	Definition
00	14"
01	15"
10	17"

Board ID [4:3]	Definition
00	Pavilion
01	Envy
10	Pavilion Special Edition

Board ID [7:5]	Definition
000	Reserve

SATA HDD

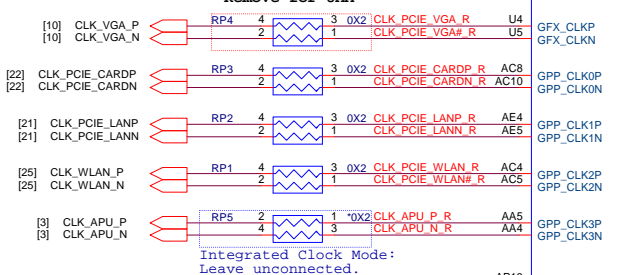


SATA ODD

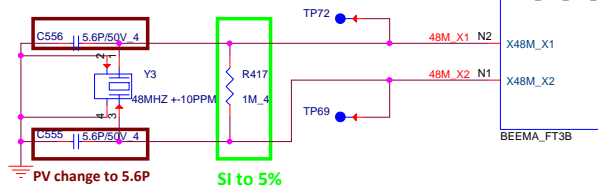


Use with external clock generator only
Integrated Clock Mode:
Leave unconnected.

Remove for UMA



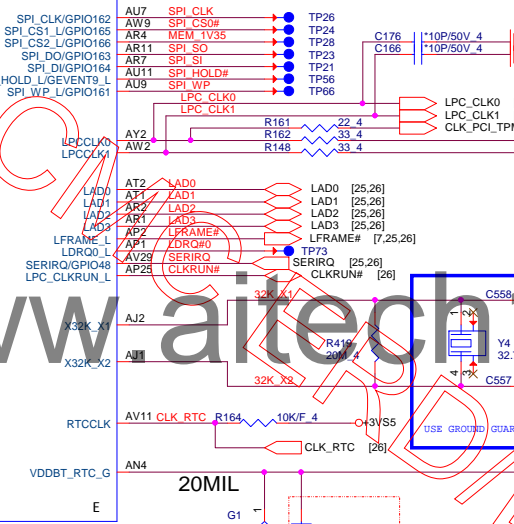
Integrated Clock Mode:
Leave unconnected.



SI to 5%

PV change to short pad

SD CARD



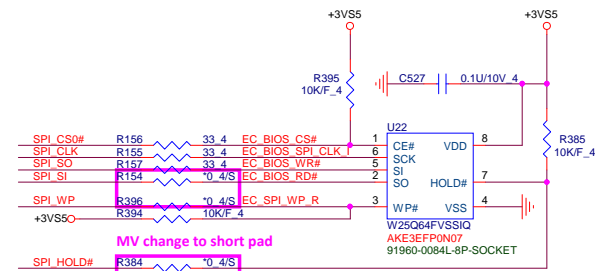
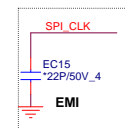
20MIL

Place C593 close to PIN

SI size to 0603

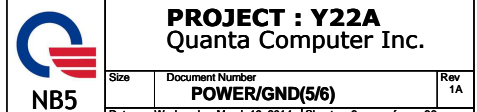
APU SPI ROM 8M SPI EC ROM

Vender	Size	P/N
WND	8M	AKE3EFP0N07
GGD	8M	AKE3EGN0Q01
EON	8M	AKE3EZN0Q01
Socket		DFHS08FS023



PROJECT : Y22A
Quanta Computer Inc.

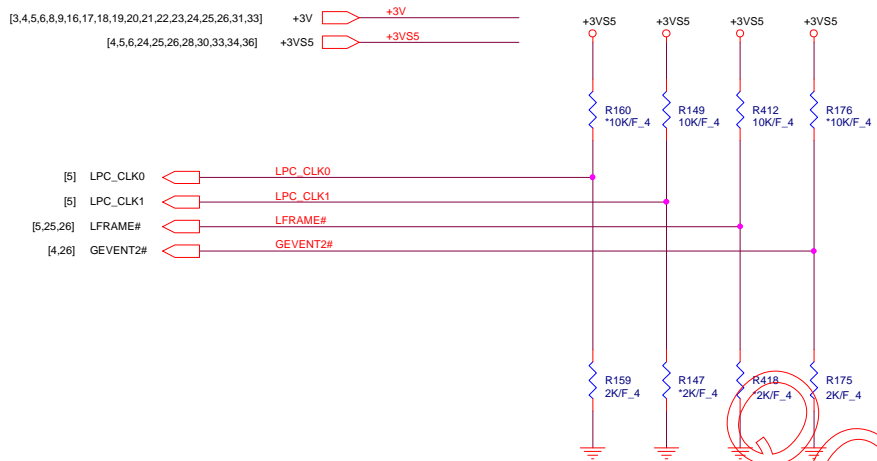
Size	Document Number	Rev
	SATA/CLK (4/6)	1A
Date: Thursday, March 27, 2014	Sheet 5 of 36	





STRAPS PINS

OVERLAP COMMON PADS WHERE POSSIBLE FOR DUAL-OP RESISTORS.

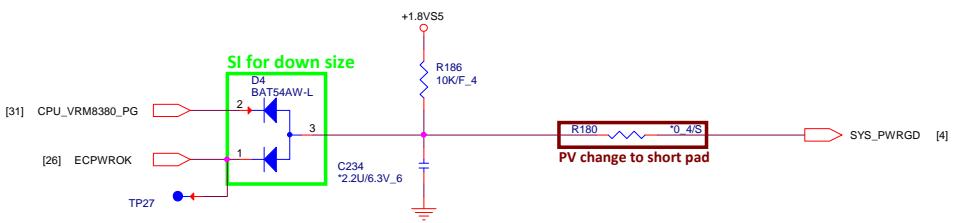


REQUIRED STRAPS

					LPC_CLK0	LPC_CLK1	LFRAME#	GEVENT2#
PULL HIGH					BOOT FAIL TIMER ENABLED	CLKGEN ENABLED DEFAULT	SPI ROM DEFAULT	1.8V SPI ROM
PULL LOW					BOOT FAIL TIMER DISABLED DEFAULT	CLKGEN DISABLED	LPC ROM	3.3V SPI ROM DEFAULT

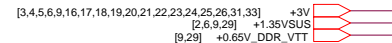
DEBUG STRAPS

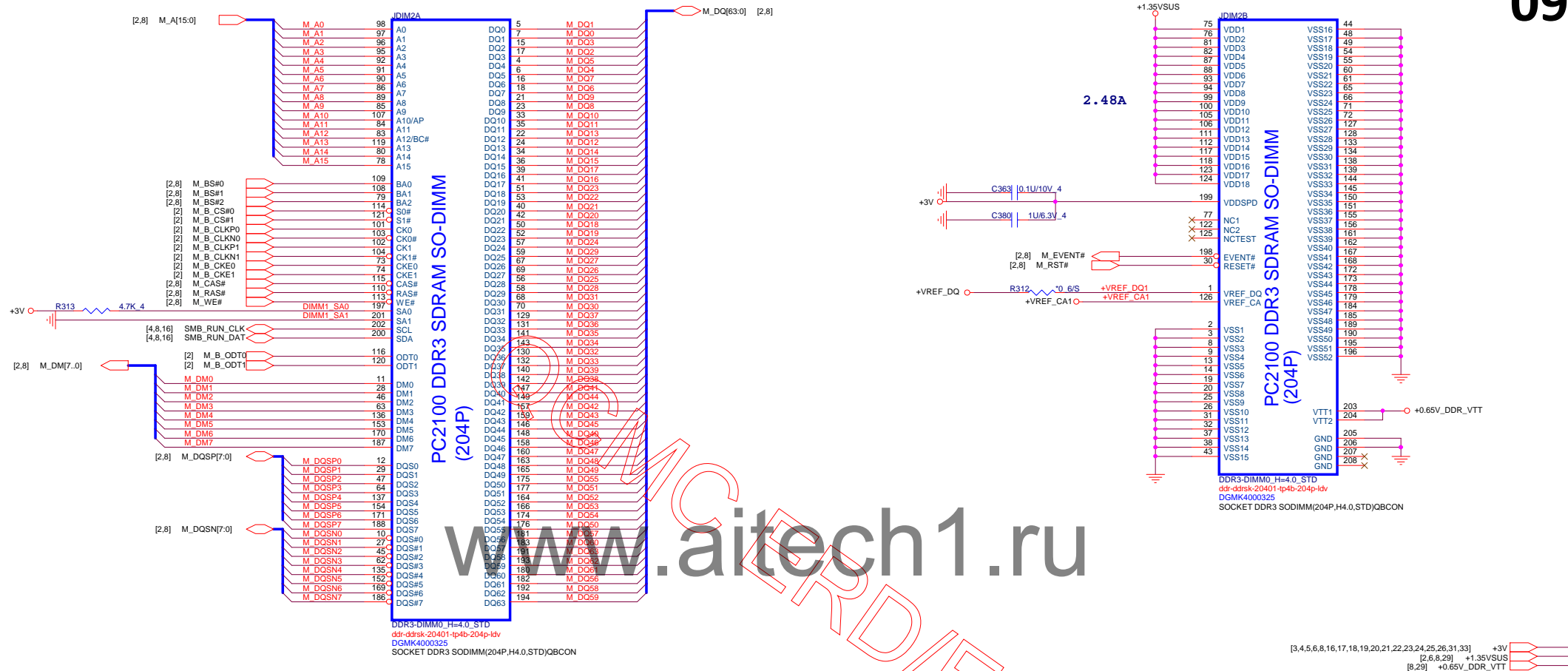
SYS_PWRGD



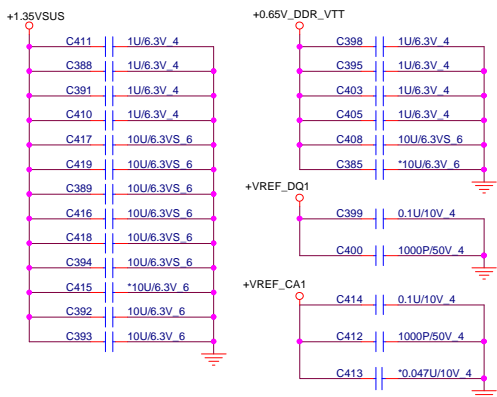
PROJECT : Y22A
Quanta Computer Inc.

Size	Document Number	Rev
	STRAP (6/6)	1A
Date: Wednesday, March 19, 2014 Sheet 7 of 36		

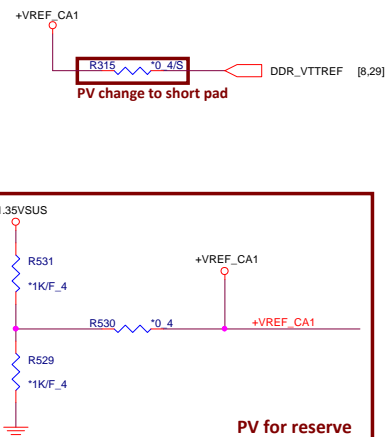
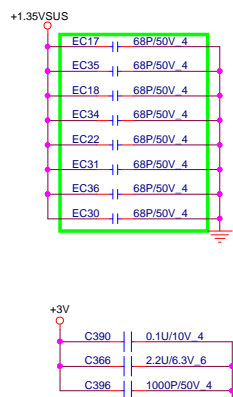




Place these Caps near So-Dimm1.

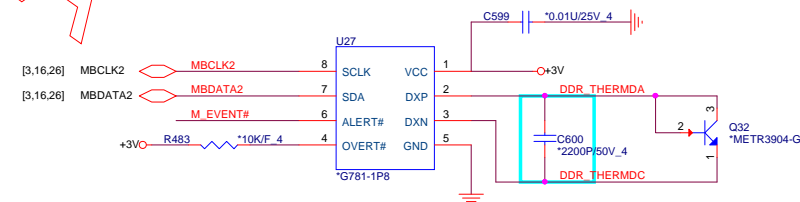


For EMI RESERVE



Local Thermal Sensor

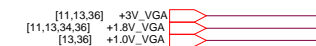
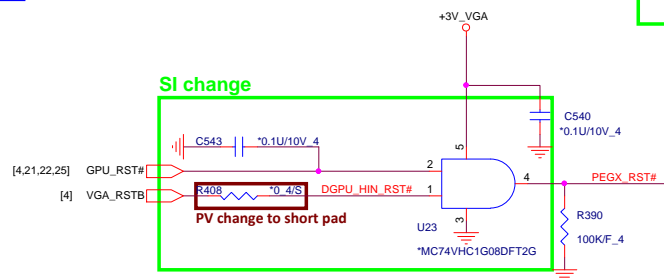
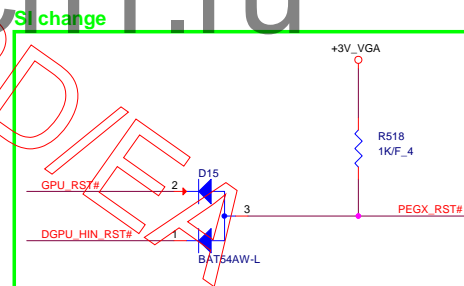
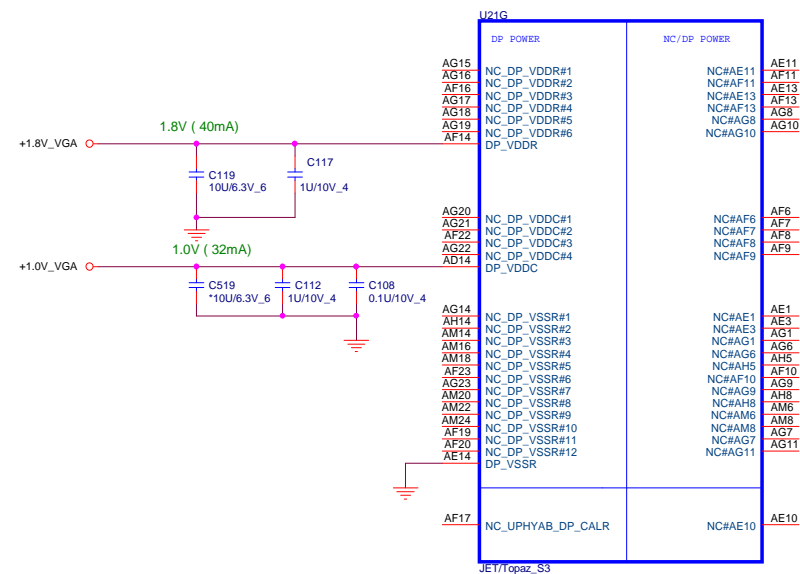
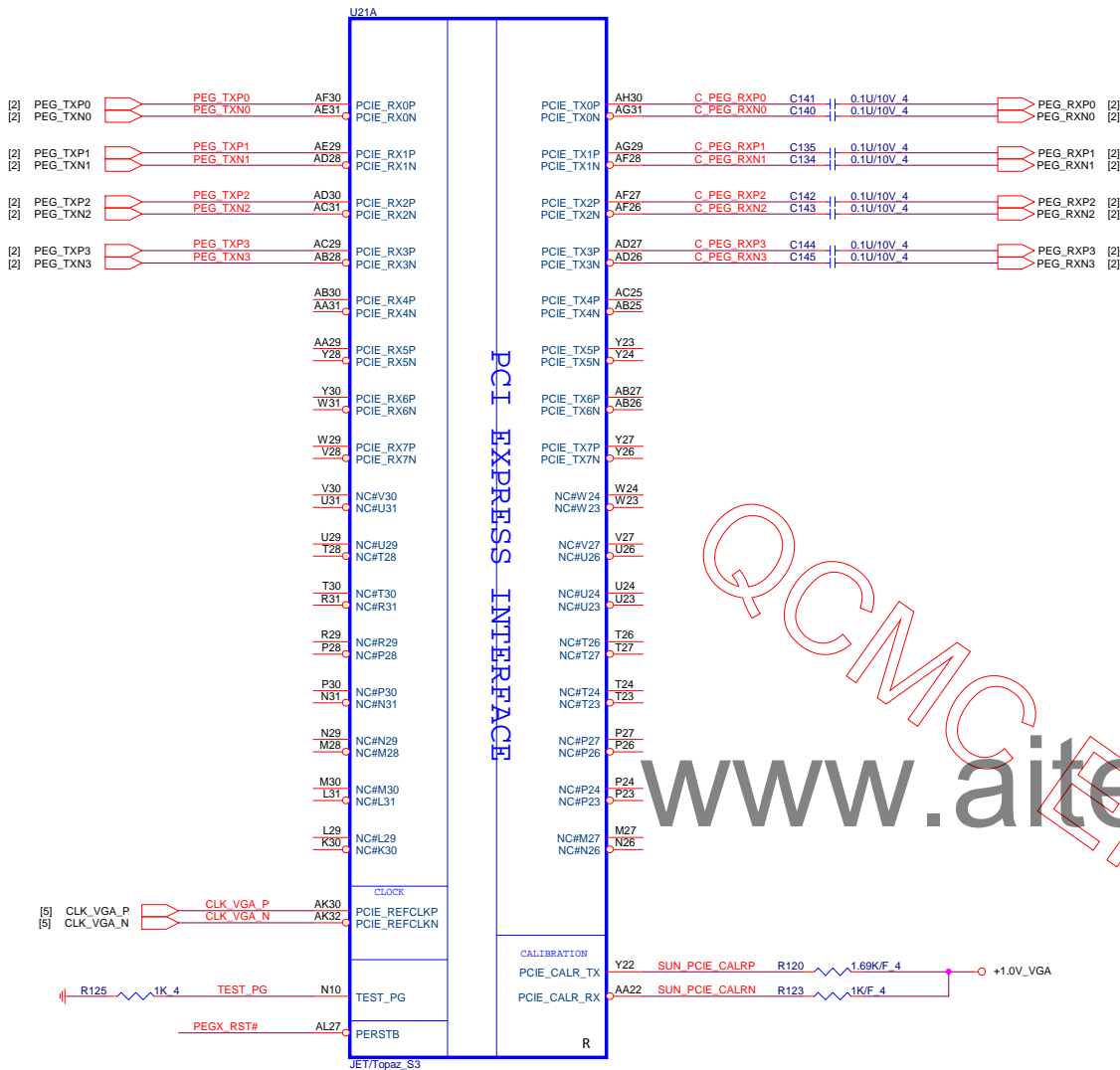
DDR3 Thermal Sensor

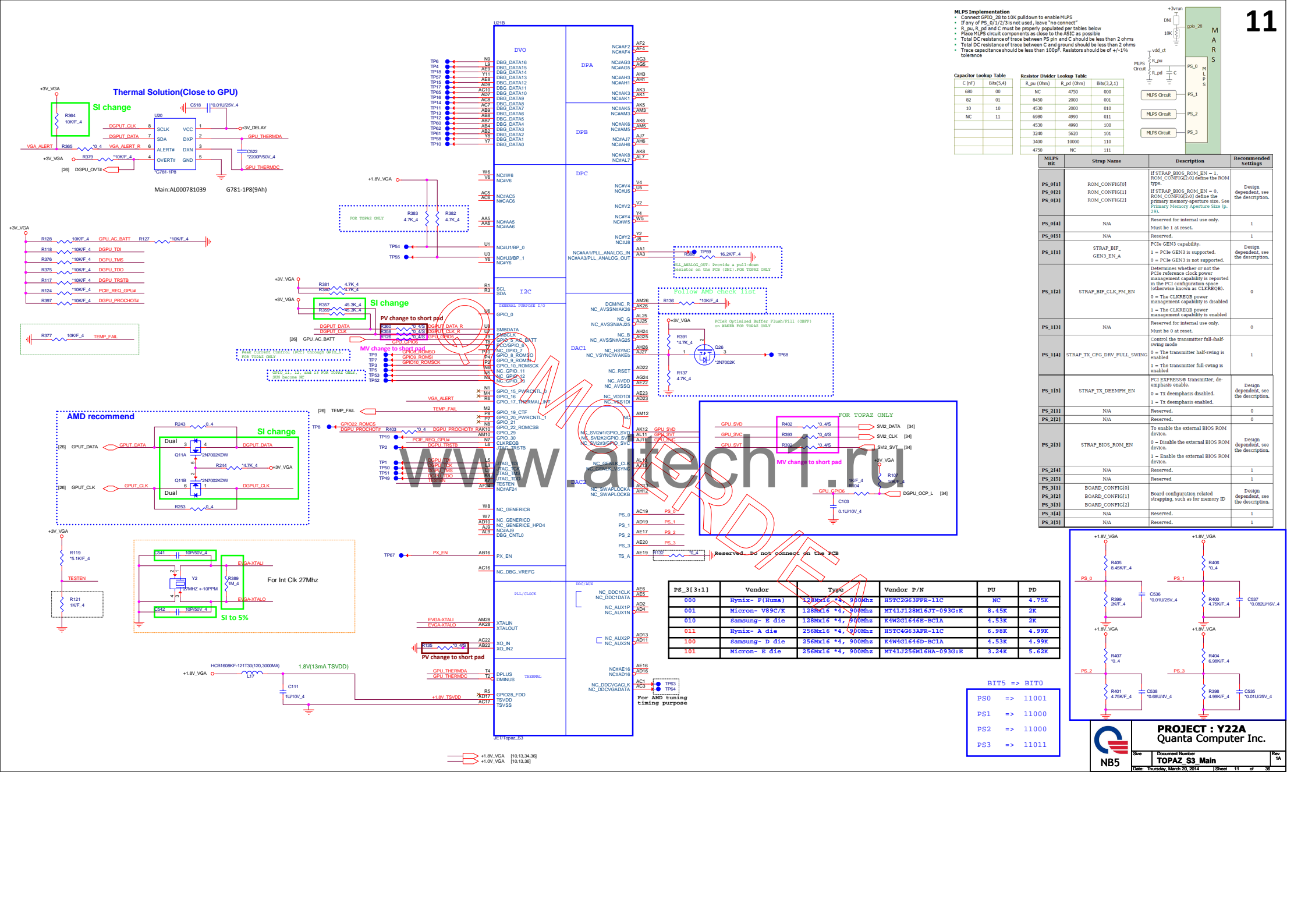


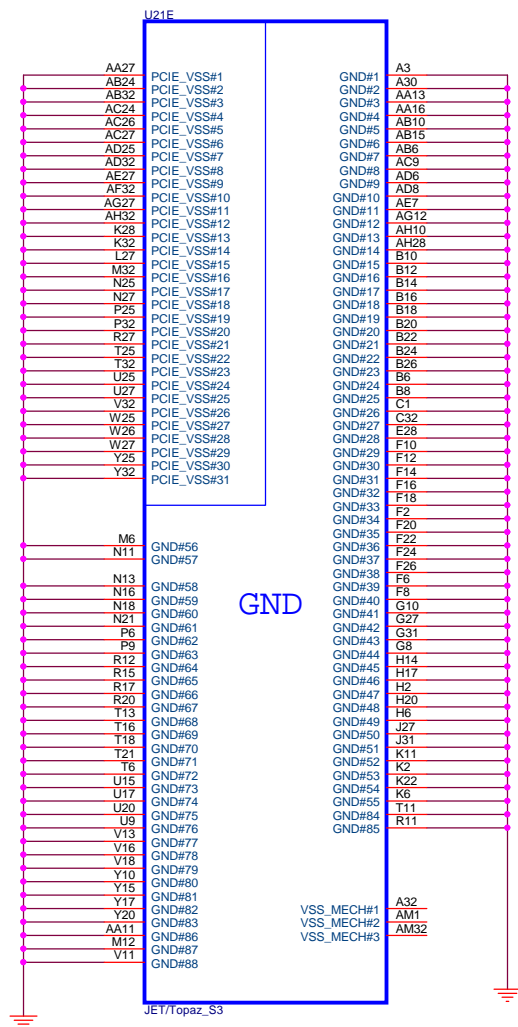
Main:AL000781039 G781-1P8(9Ah)
2nd:AL001412005 EMC1412-2-ACZL-TR(9Ah)

Main:AL001412003 EMC1412-1-ACZL-TR(98h)
2nd:AL000431014 TMP431ADGKR(98h)

PROJECT : Y22A Quanta Computer Inc.		
NB5	Size	Document Number
	Custom	System Memory 2/2 (4.0H)
	Date: Wednesday, March 19, 2014	Sheet 9 of 36







CONFIGURATION STRAPS-- SEE EACH DATABOOK FOR STRAP DETAILS

ALLOW FOR PULLUP PADS FOR THESE STRAPS AND IF THESE GPIOs ARE USED, THEY MUST NOT CONFLICT DURING RESET

RECOMMENDED SETTINGS
0= DO NOT INSTALL RESISTOR
1 = INSTALL 3K RESISTOR
X = DESIGN DEPENDANT
NA = NOT APPLICABLE

STRAPS	PIN	DESCRIPTION OF DEFAULT SETTINGS	
TX_PWRS_ENB	GPIO0	PCIE FULL TX OUTPUT SWING	0
TX_DEEMPH_EN	GPIO1	PCIE TRANSMITTER DE-EMPHASIS ENABLED	X
RSVD	GPIO2	RESERVED	0
RSVD	GPIO8	RESERVED	0
BIF_VGA_DIS	GPIO9	VGA ENABLED	0
RSVD	GPIO21	RESERVED	0
BIOS_ROM_EN	GPIO_22_ROMCSB	ENABLE EXTERNAL BIOS ROM	0
ROMIDCFG(2:0)	GPIO[13:11]	SERIAL ROM TYPE OR MEMORY APERTURE SIZE SELECT	0 0 1
VIP_DEVICE_STRAP_ENA	V2SYNC	IGNORE VIP DEVICE STRAPS (Removed on Seymour/W/histler)	0
RSVD	H2SYNC	RESERVED	0
AUD[1] AUD[0]	HSYNC VSYNC	SEE DATABOOK FOR DETAIL SEE DATABOOK FOR DETAIL	0 0
RSVD	GENERICC	RESERVED	0

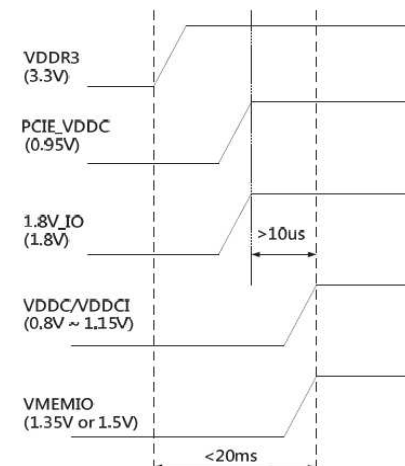
NOTE1: AMD RESERVED CONFIGURATION STRAPS

ALLOW FOR PULLUP PADS FOR THESE STRAPS BUT DO NOT INSTALL RESISTOR. IF THESE GPIOs ARE USED, THEY MUST KEEP "LOW" AND NOT CONFLICT DURING RESET.

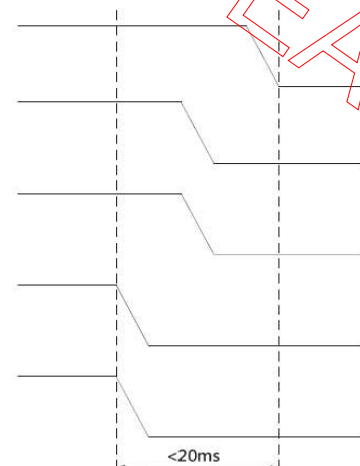
GPIO21 H2SYNC GENERICC GPIO8 GPIO2

POWER UP / POWER DOWN SEQUENCE

POWER UP



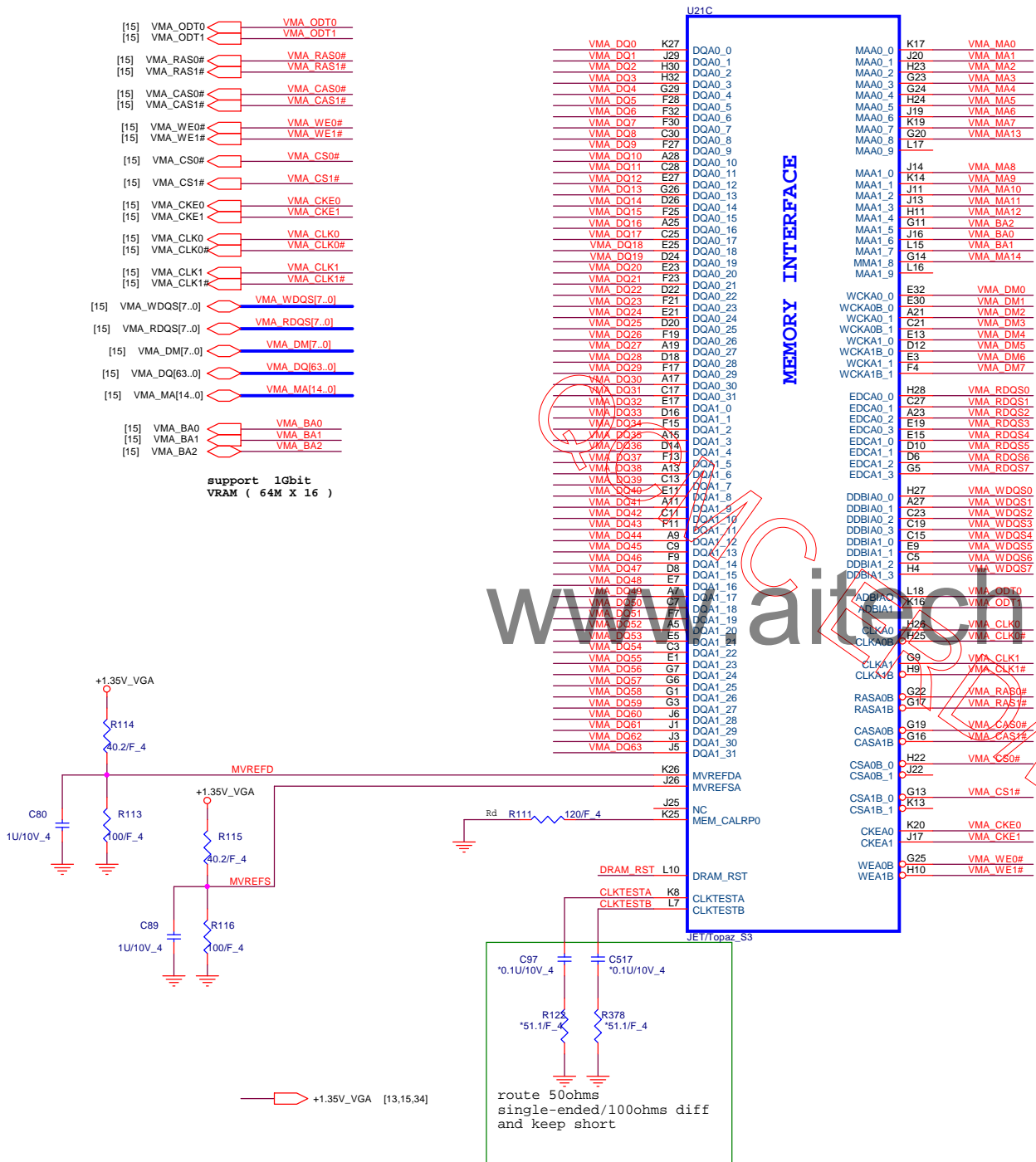
POWER DOWN

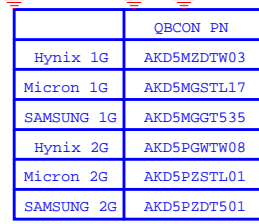


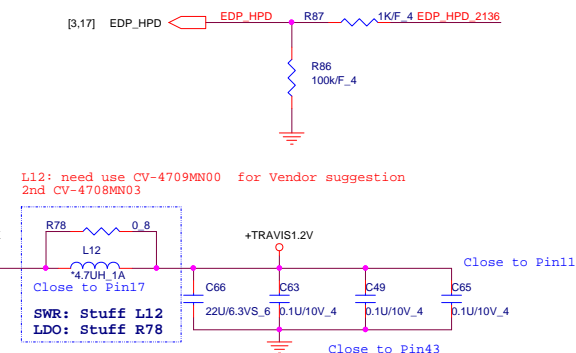
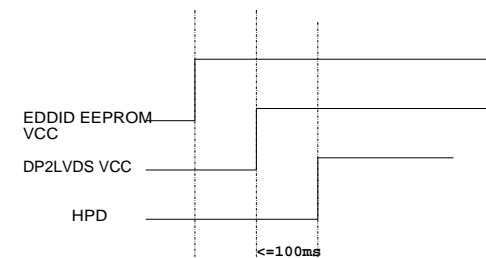
PROJECT : Y22A
Quanta Computer Inc.

Size	Document Number	Rev
	TOPAZ_S3_GND/LVDS/Strap	1A
Date: Wednesday, March 19, 2014	Sheet 12 of 36	

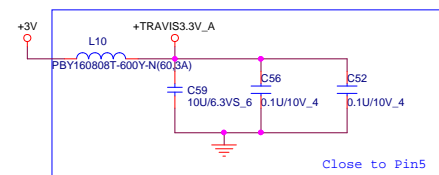
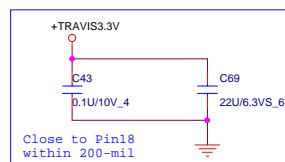
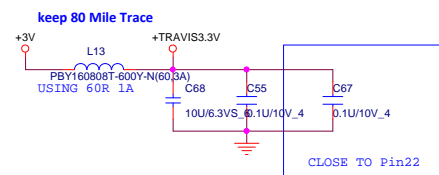
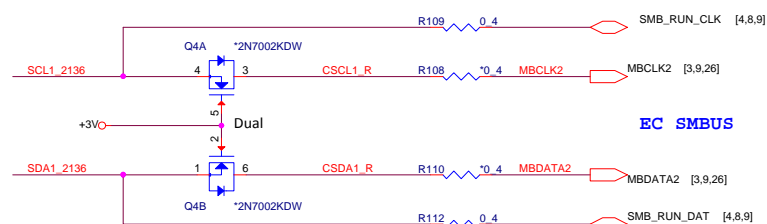


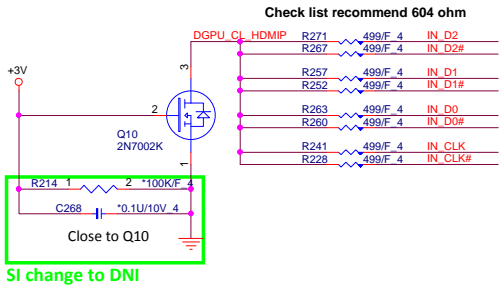
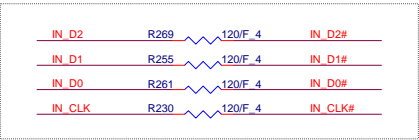
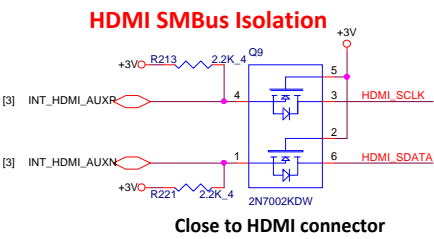




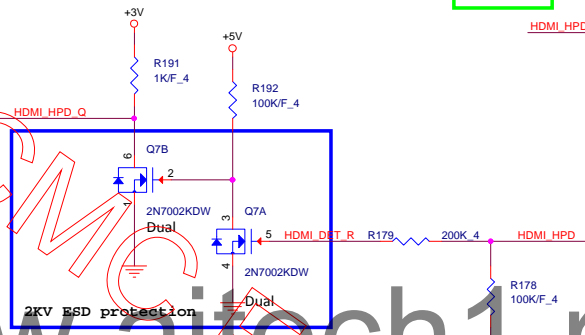


		MODE_CFG0(PIN47)	
		0	1
MODE_CFG1(PIN48)	0	X	EP MODE
	1	ROM ONLY MODE	EEPROM MODE

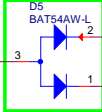




HDMI HPD SENSE



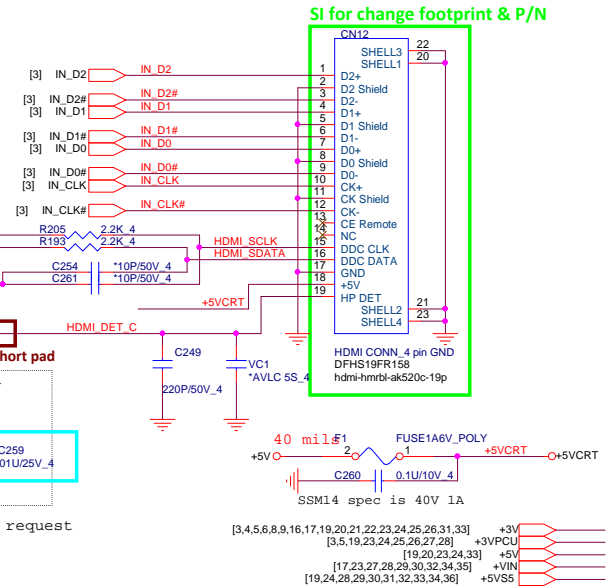
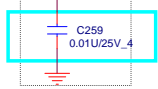
SI for down size



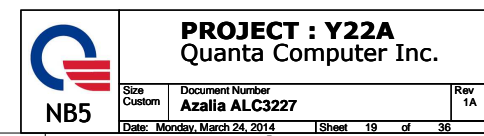
PV change to short pad

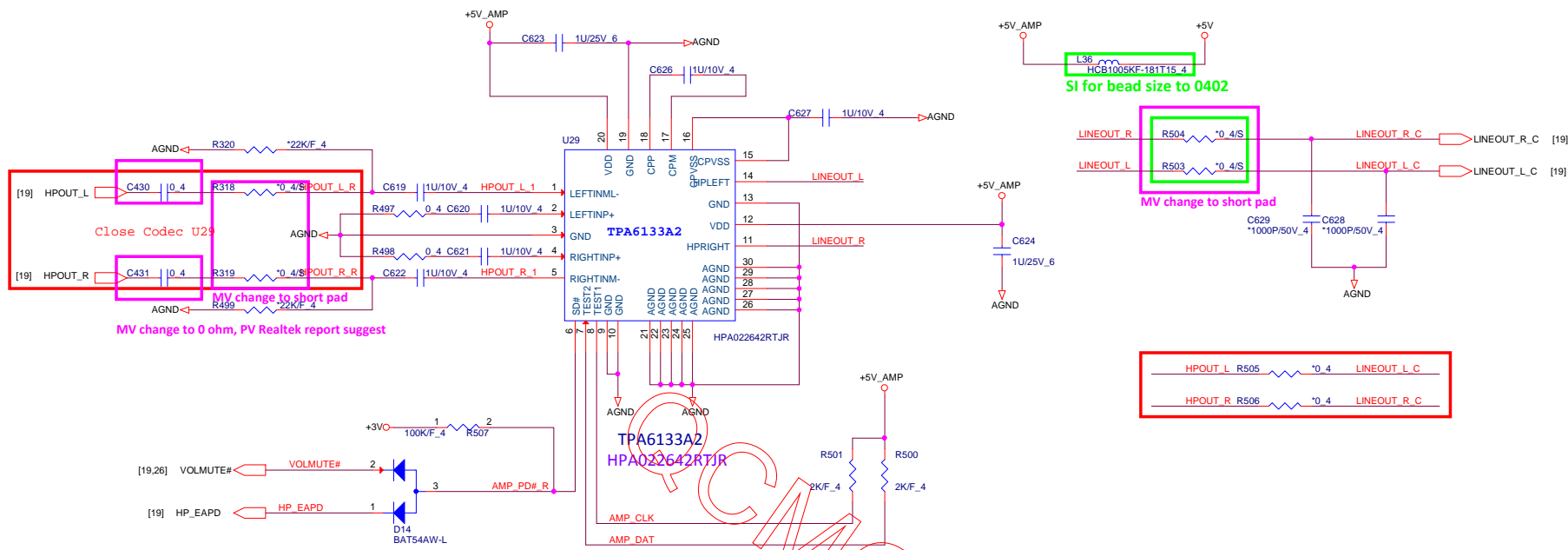


for EMI request

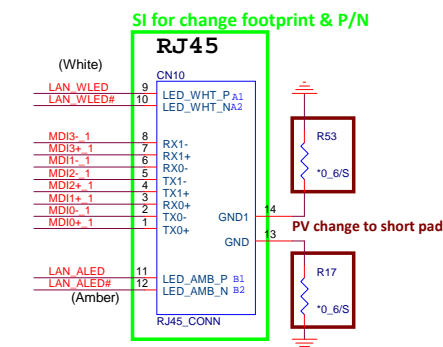
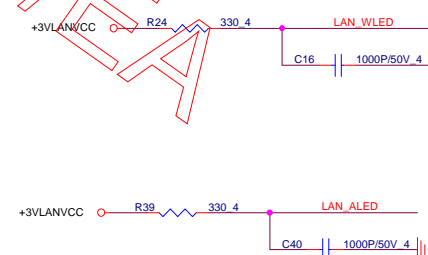
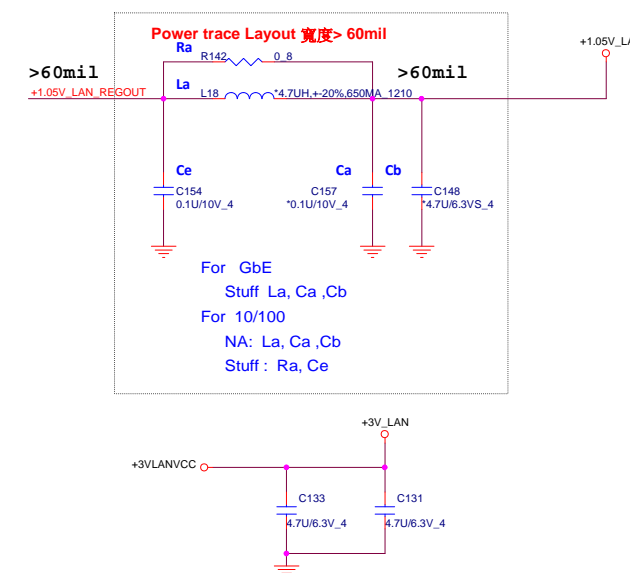


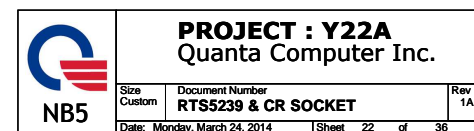
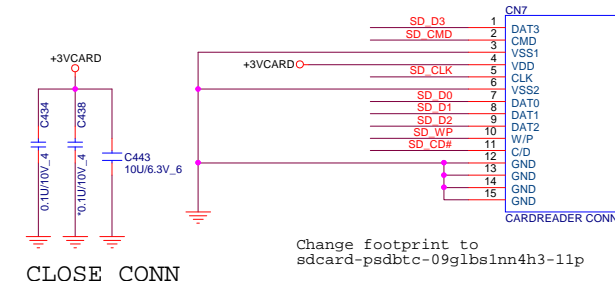
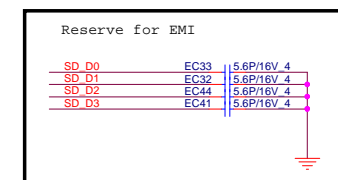
www.aitech1.ru



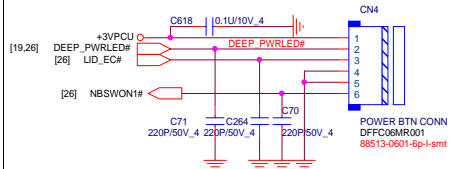


www.aitech1.ru

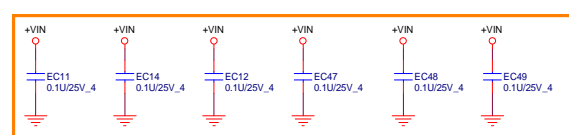
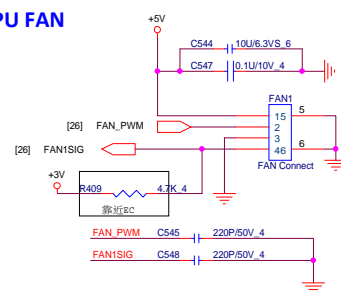




Power Button Connector



CPU FAN

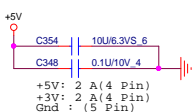
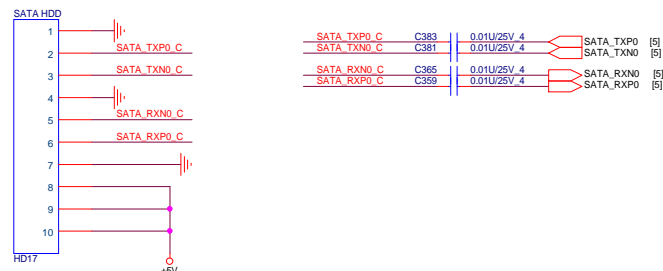


FOR EMI

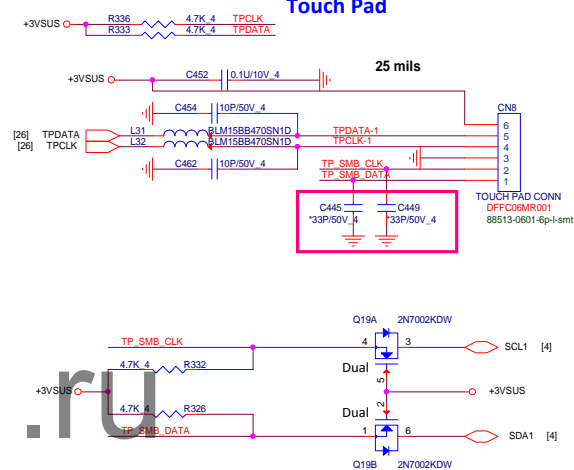
[17,27,28,29,30,32,34,35]

+VIN

SATA HDD Connector(Cable type)



Touch Pad

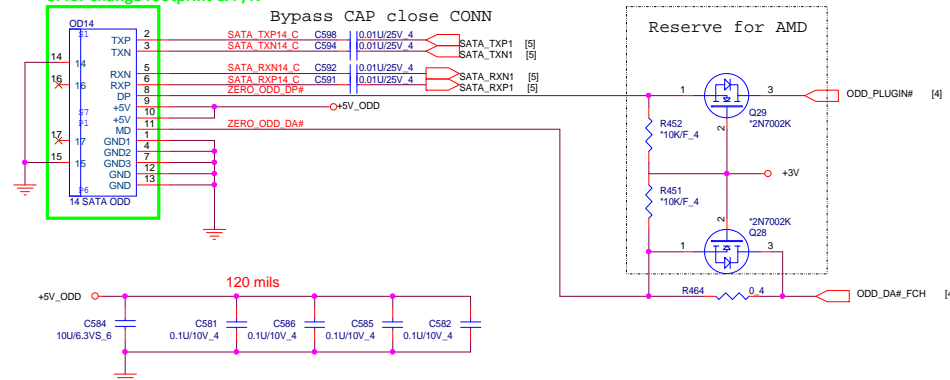


SATA ODD CONNECTOR

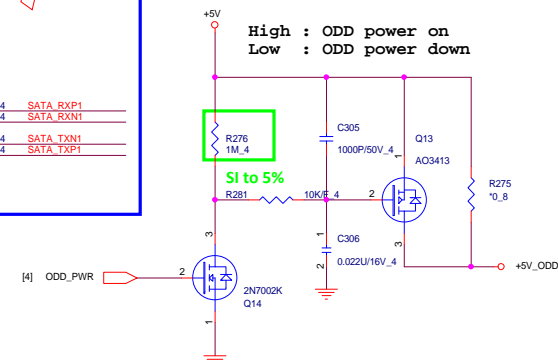
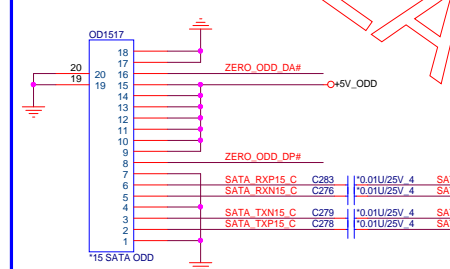
SI for change footprint & P/N

Bypass CAP close CONN

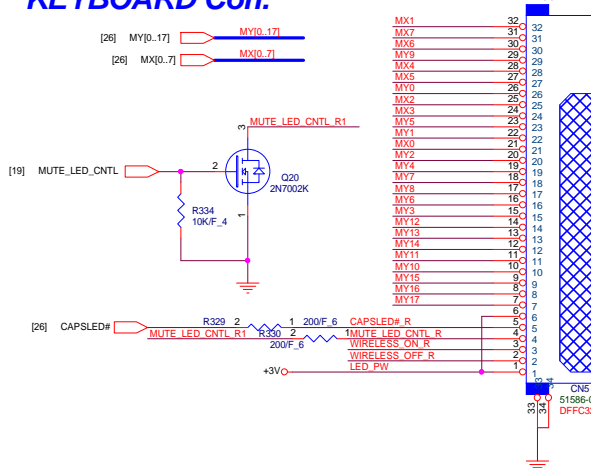
Reserve for AMD



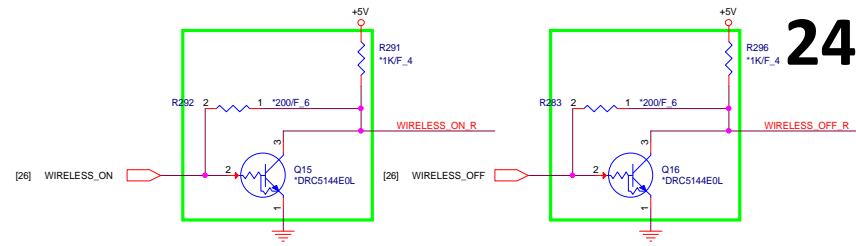
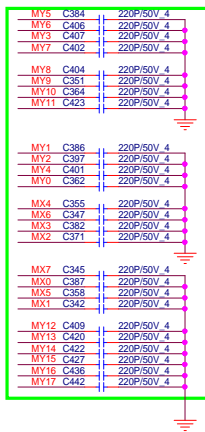
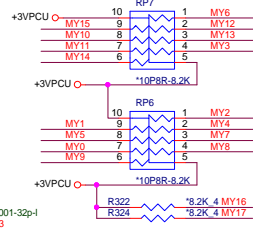
15" & 17" SATA ODD



KEYBOARD CON.

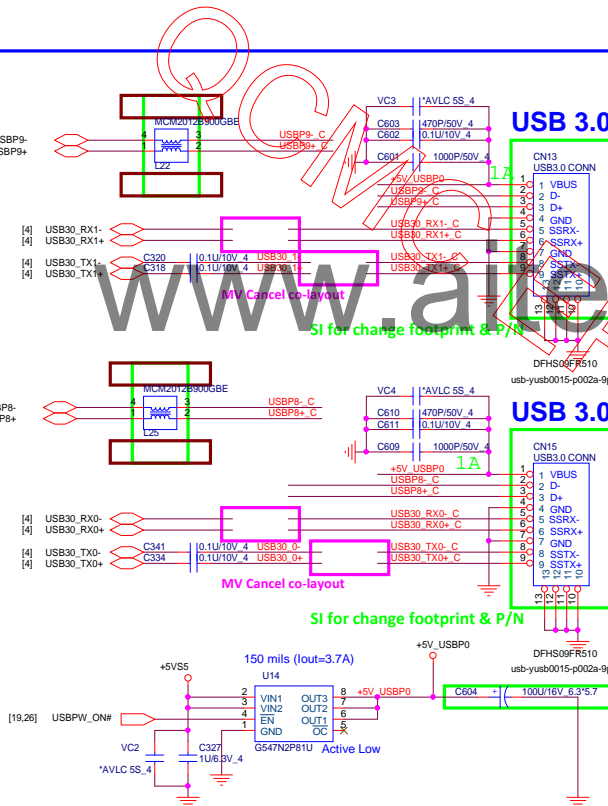
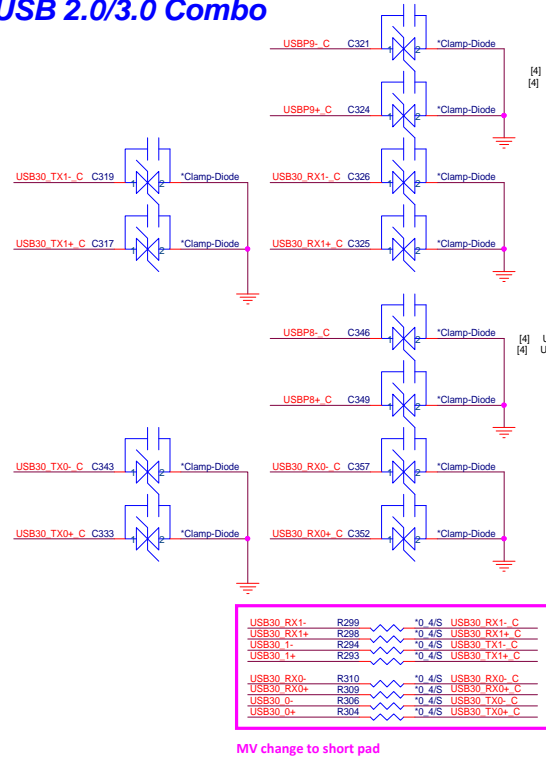


KEYBOARD PULL-UP

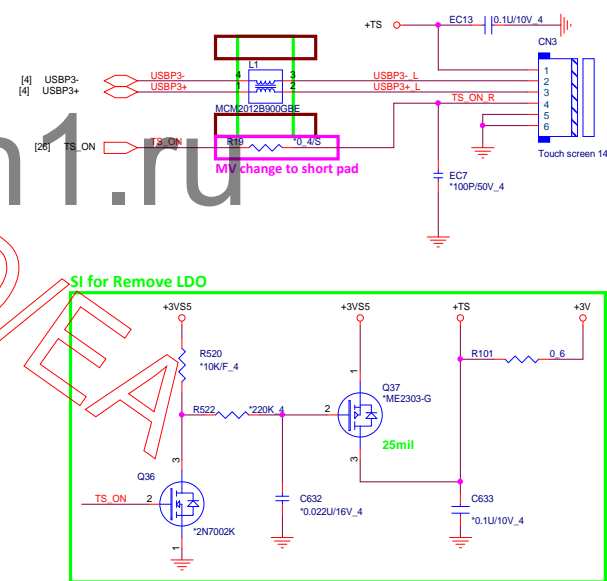


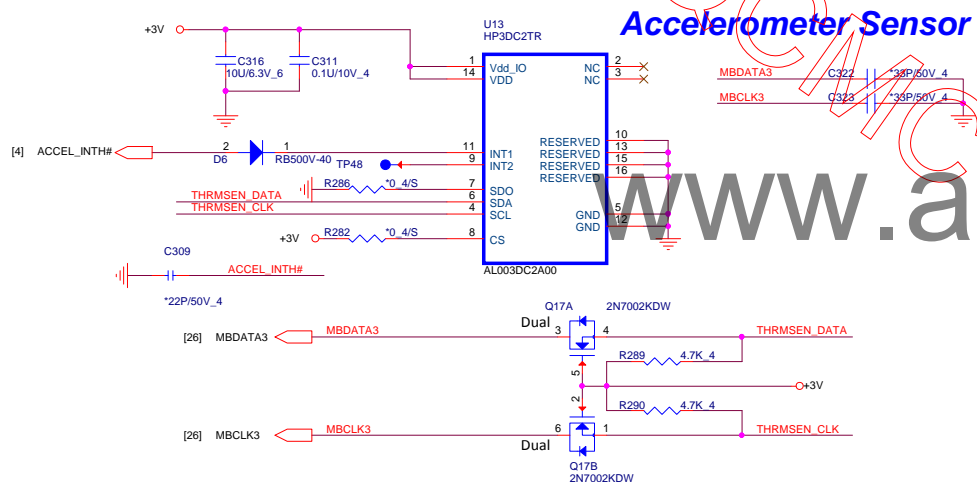
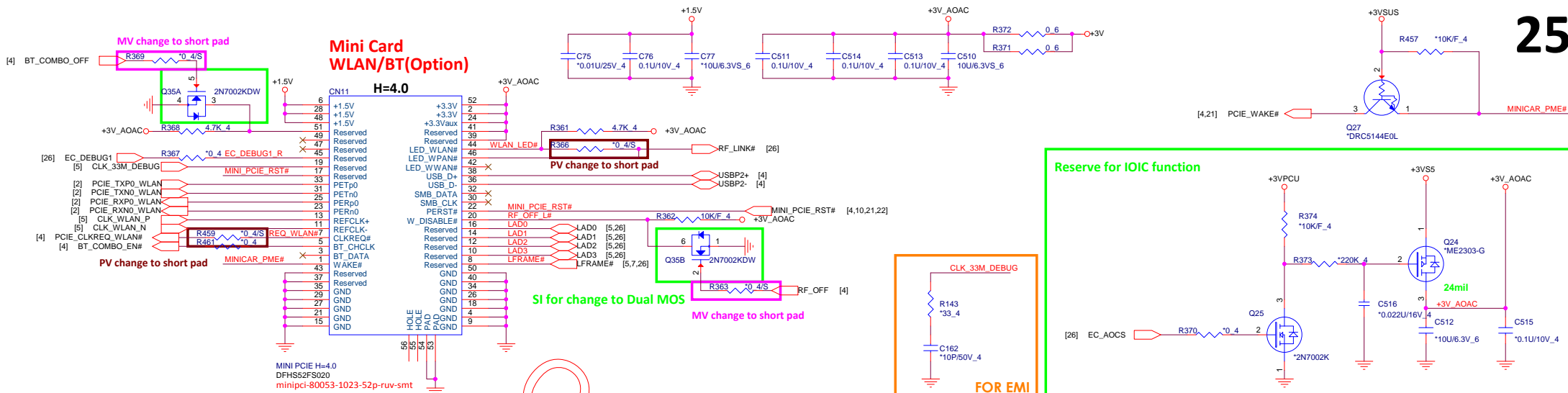
24

USB 2.0/3.0 Combo

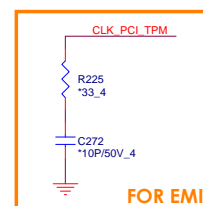
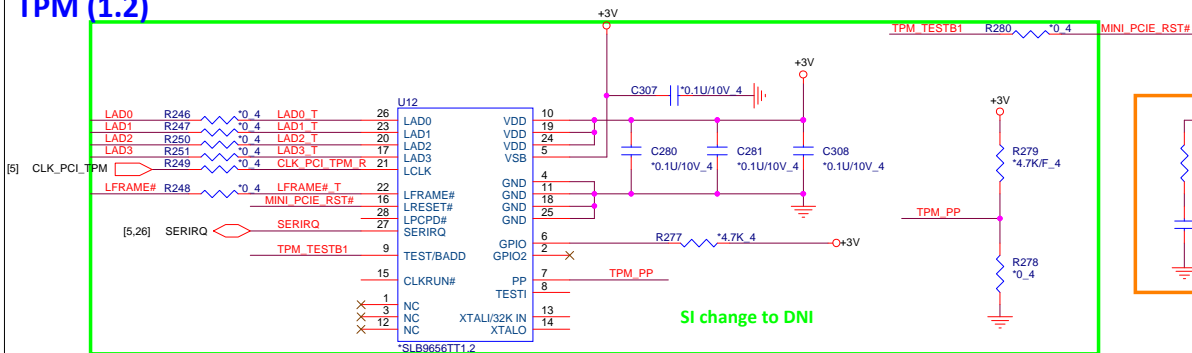



Touch screen

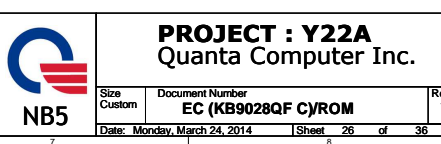
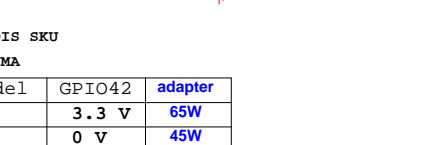
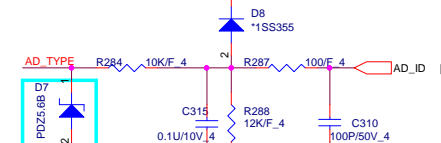
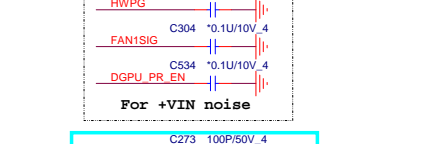
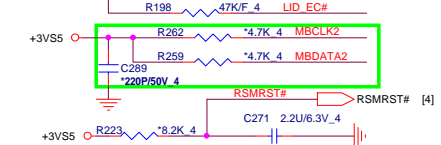
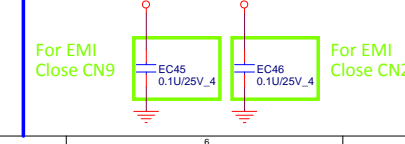
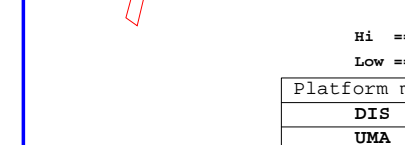
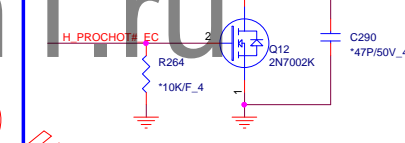
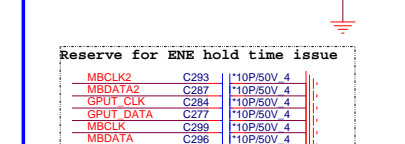
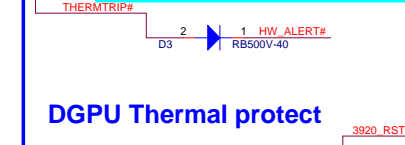
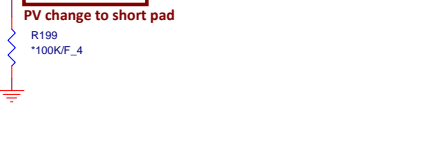
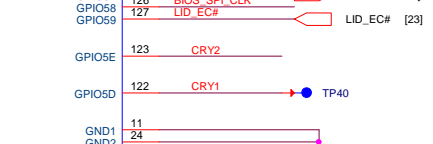
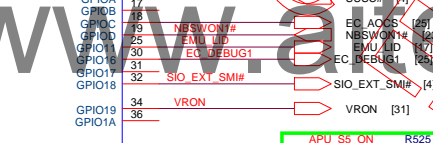
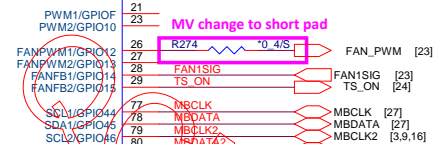
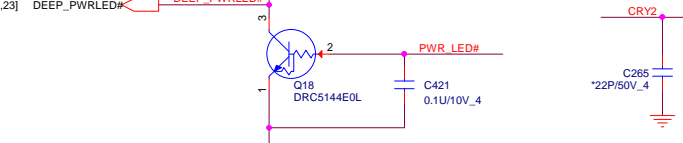
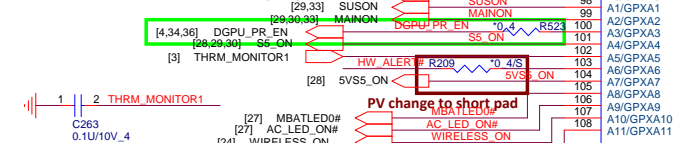
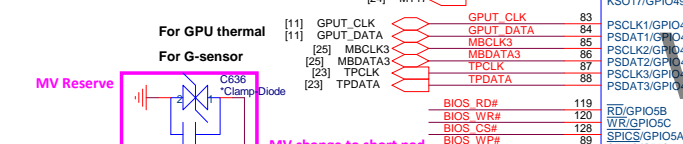
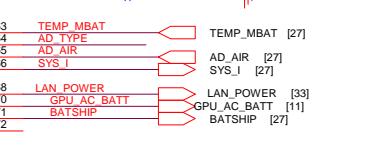
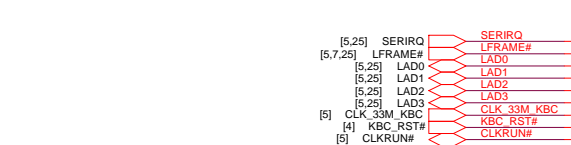


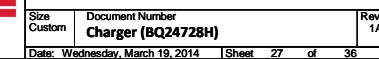


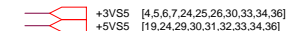
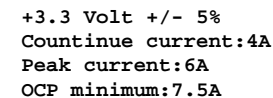
TPM (1.2)



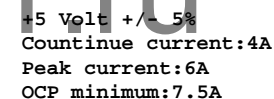
	PROJECT : Y22A Quanta Computer Inc.		
	Size Custom	Document Number WLAN/G-Sensor/GCLK/TPM	Rev 1A
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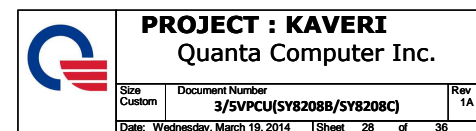




www.aitech1.ru +5 Volt +/- 5V

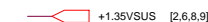


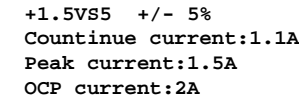
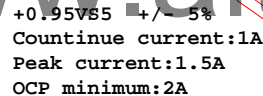
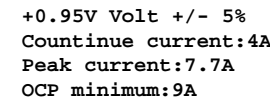
USB Charge support	Ra	Rb
Vine (No support)	Stuff	NA
Envy (Support)	NA	Stuff

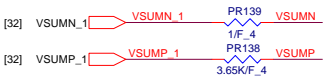


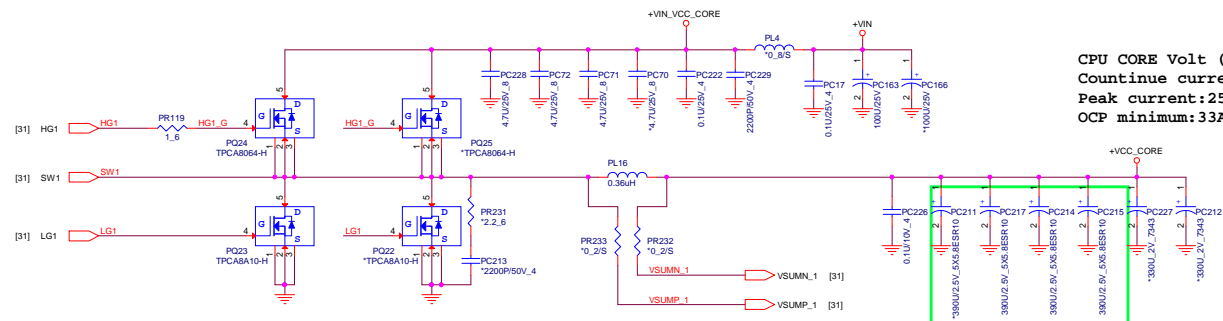


1.8VS5 +/- 3%
Continue current:2A
Peak current:3A
OCP minimum:4A

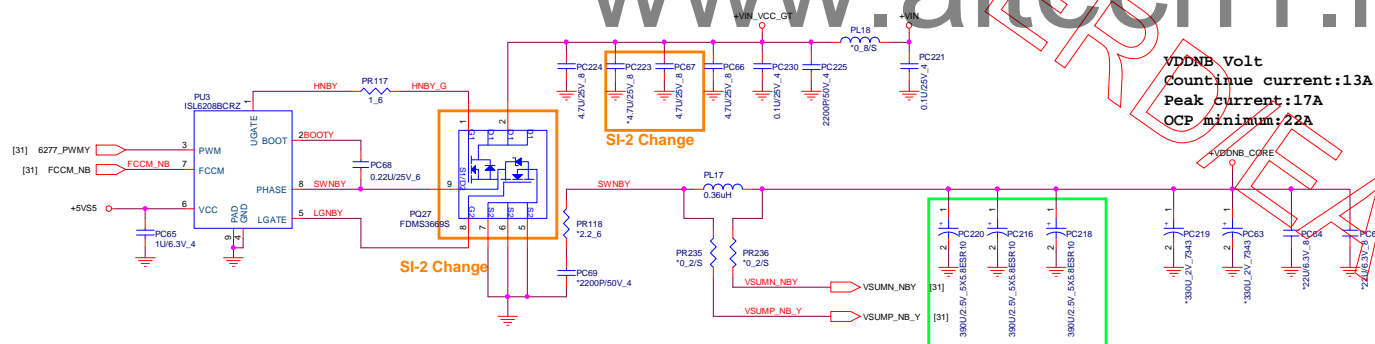


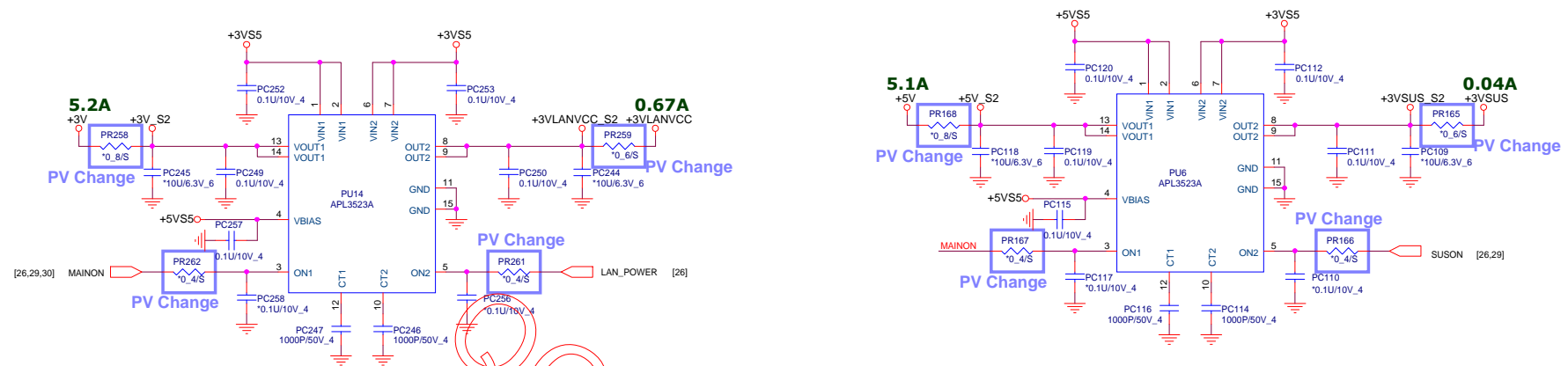




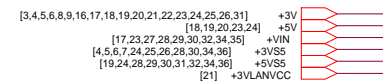
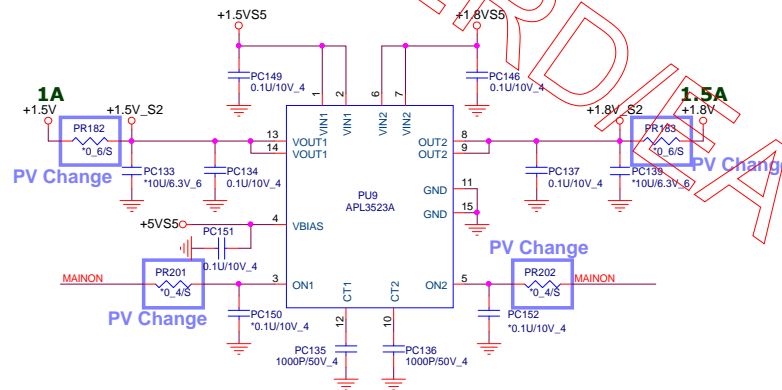


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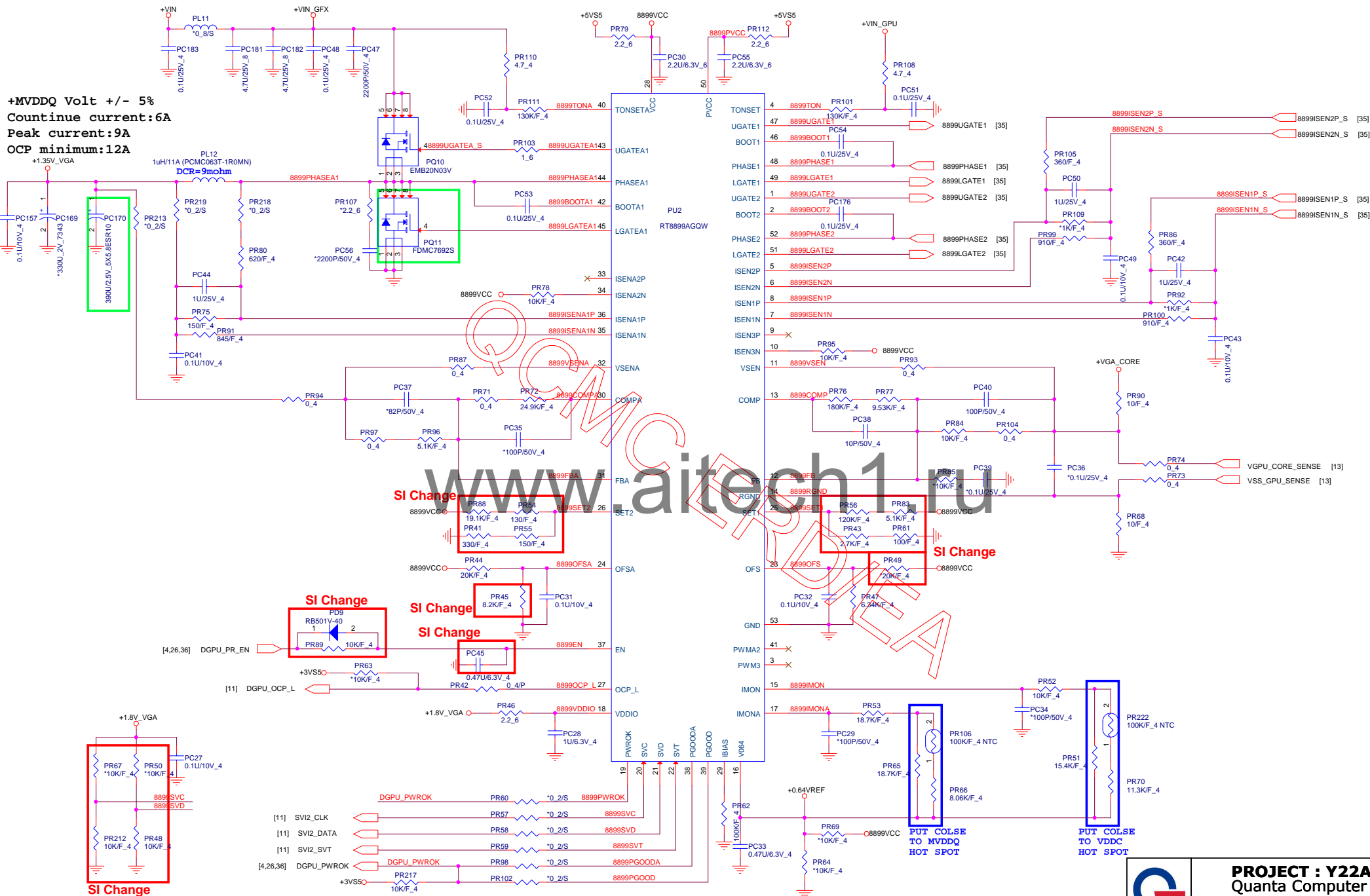
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Quanta Computer Inc.

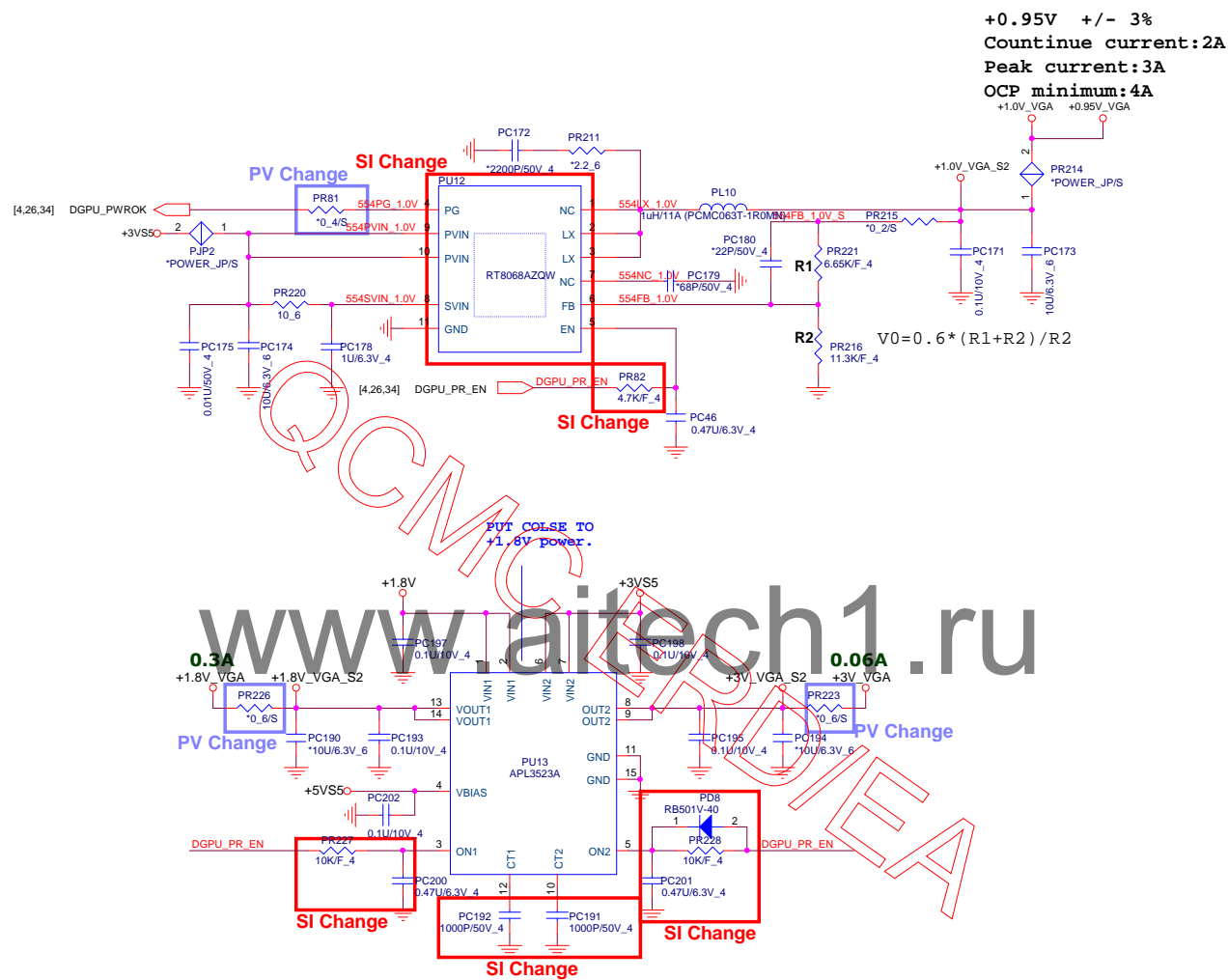
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+MVDDQ Volt +/- 5%
 Countinue current:6A
 Peak current:9A
 OCP minimum:12A



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 Quanta Computer Inc.

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+3V_VGA [10,11,13]
+1.0V_VGA [10,13]
+1.8V_VGA [10,11,13,34]